

Amendments to the Specification:

Please insert the following two paragraphs after line 21 page 5:

Figure **4A** illustrates disk drive **10** including voice coil actuator **30** comprising a head arm **150** of Figure **3B**; and

Figure **4B** illustrates disk drive **10** including voice coil actuator **30** comprising head arms **150**, **152**, and **154** of Figure **3B**.

Please insert the following paragraph after line 24 page 5:

In Figure **3A**, the head arm **150** includes at least one ground plane formed in the head arm, using the metallic body of the head arm. The head arm includes a first and a second pair of coplanor, parallel transmission paths **174-180** essentially parallel to the ground plane. The head arm **150** may further include a third and a fourth pair of coplanor, parallel transmission paths **182-188** essentially parallel to the ground plane.

Please insert the following paragraph after line 16 page 6:

As used herein, the first head arm refers to the head arm **152**. The head arm **150** refers to a second head arm. The head arm **154** refers to a third head arm. The head arm collection includes the first, second, and third head arms.

Please insert the following eight paragraphs after line 18 page 7:

Figure **4A** illustrates disk drive **10** including a voice coil actuator arm **30** comprising a head arm **150** of Figure **3B**.

Figure **4B** illustrates disk drive **10** including the voice coil actuator arm **30** comprising head arms **150**, **152**, and **154** of Figure **3B**.

In Figures **4A** and **4B**, each of the head arms **150**, **152**, and **154** provides at least one ground plane formed in said head arm by its metallic body. Each of the head arms **150**, **152**, and **154** includes a first pair of coplanor, parallel transmission paths **174** and **176** as

well as, a second pair of coplanar, parallel transmission paths **178** and **180** essentially parallel to said ground plane.

In Figures **4A** and **4B**, the head arm **150** interconnects the first pair of coplanar, parallel transmission paths **174** and **176** by a read differential wire pair **300** and **302** to a head slider **60**, and to a disk drive read interface **200**. The head arm **150** interconnects the second pair of coplanar, parallel transmission paths **178** and **180** by a write differential wire pair **350** and **352** to a head slider **60** and to a disk drive write interface **250**.

In Figure **4B**, the second head arm **152** interconnects the first pair of coplanar, parallel transmission paths **174** and **176** by a read differential wire pair to a head slider **62**, and to a disk drive read interface **202**. The head arm **152** interconnects the second pair of coplanar, parallel transmission paths **178** and **180** by a write differential wire pair to a head slider **60** and to a disk drive write interface **252**.

In Figure **4B**, the first head arm **152** also includes a third coplanar, parallel transmission paths **182** and **184** as well as, a fourth pair of coplanar, parallel transmission paths **186** and **188**. Both third and fourth pairs of coplanar, parallel transmission paths are essentially parallel to the ground plane. The third coplanar, parallel transmission paths **182** and **184** interconnecting both a second read differential wire pair to a second head slider **64**, and to a second disk drive read interface **204**. The fourth pair of coplanar, parallel transmission paths **186** and **188** interconnect a second write differential wire pair to a second head slider **64**, and to a second disk drive read interface **254**.

In Figure **4B**, the third head arm **154** interconnects the first pair of coplanar, parallel transmission paths **174** and **176** by a read differential wire pair to a head slider **66**, and to a disk drive read interface **206**. The head arm **150** interconnects the second pair of coplanar, parallel transmission paths by a write differential wire pair to a head slider **66** and to a disk drive write interface **256**.